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026304 7590 11/14/2005

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Customer Number: 026304
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01 FC:1501 1400.00 DA
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10736,934

12/16/2003

FIRST NAMED INVENTOR

Hisashi Yamaguchi

ATTORNEY DOCKET NO.

NEC 15.938A

CONFIRMATION NO.

5519

TITLE OF INVENTION: METHOD OF GENERATING TEST PATTERN FOR INTEGRATED CIRCUIT

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	02/14/2006

EXAMINER	ART UNIT	CLASS-SUBCLASS
LAMARRE, GUY J	2133	714-727000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

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NEC Electronics Corporation

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Kawasaki, Japan

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Brian E. Hennings

Date

January 31, 2006

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51,271

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To	Company	Fax Number	Phone Number
1 Commissioner for Patents	United States Patent and Trademark Office	(571) 273-2885	

Date	Client / Matter
January 31, 2006	100806-00244
From	Attorney Number
James J. Dade	03290
Phone	Fax
(212) 940-6754	(212) 940-8986

Total number of pages, including cover letter: 3
If you do not receive all of the pages, please call: (212) 940-8755

U.S. Patent Application No.: 10/736,934

METHOD OF GENERATING TEST PATTERN FOR ...

Our Reference No.: NEC 15.938A (100806-00244)

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